

```
/******
```

Timer V control

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November 4,2001 Separate Timer V control routine

November 17,2001

July 19,2004

November 7,2004 Start with New PCB, disable P76 toggle output

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```
*****/
```

```
// #pragma interrupt timerV_irq(VECT=22)
```

```
/* ----< Include Files > ----- */
```

```
#include "3664f.h"
```

```
/* ----< Function Prototype > ----- */
```

```
void reset_timerV(void);
```

```
/* ----< Control program > ----- */
```

```
/* TIMERV Initialize routine */
```

```
void reset_timerV()
```

```
{
```

```
    TCRVO.BIT.CMIEA = 0x00;          /* Disable interrupt */
```

```
    TCRVO.BIT.CCLR = 0x01;          /* Clear TCNTV/Compare match A */
```

```
    TCRVO.BIT.CKS = 0x03;          /* 16MHz/(64*125) = 2KHz(500uS) */
```

```
    TCRV1.BIT.ICKS0 = 0x02;        /* Clock = 1/64 */
```

```
//    TCNTV = 125;                  /* 500uS interval */
```

```
    TCORA = 125;                   /* compare 500uS data */
```

```
//    TCSR.V.BYTE = 0x03;          // toggles output on P76/TMOV
```

```
    TCRVO.BIT.CMIEA = 0x01;        /* Enable interrupt */
```

```
}
```

```
/* TIMERV Interrupt Handler */
```

```
/* Does NOT use (use same named subroutine in rtm_H8tiny.asm)
```

```
void timerV_irq()
```

```
{
```

```
    if( TCSR.V.BIT.CMFA == 1 ){
```

```
        TCSR.V.BIT.CMFA = 0;
```

```
    }
```

```
}
```

```
*/
```