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;*****
;
;   H8 Real Time Monitor System start-up and assembler routine
;
;
;   May      25,1998      Kenji Arai Start modification for H8/3048F
;   October  3,1999      Debug C section
;   November 4,2001      start porting for H8/3664F
;   July     20,2002      Modified for Hitach C compiler
;
;
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;*****
;
;   July     17,2004      Modified for HEW3 environment
;
;
;   .cpu 300HN
;
;   .IMPORT_rtm_h8
;   .IMPORT _ccr_save
;
;   .EXPORT_irq_enable
;   .EXPORT_irq_disable
;   .EXPORT _timerV_irq
;
; Support for running on the ROM (Transfer initial data)
; Interrupt enable/disable
; Tick timer using timer V
; Interrupt handler for serial communication
;
;
;   ;=====
;   ;   Vector area
;   ;=====
;   .SECTION      $VECT22,DATA,LOCATE=H'2C
;   .DATA.W       _timerV_irq ;Timer V Interrupt handler
;
;   ;-----
;   ;500uSec interval Interrupt
;   ;-----
;   .SECTION      P,CODE,ALIGN=2
;
; -- Timer Interrupt Handler --
_timerV_irq:
;   bra    timV_not_rtm      ; DEBUG PURPOSE ONLY!!
;   push.l er0
;   push.l er1
;   bclr  #CMFA,@TCSRV      ; stop interrupt
;   bclr  #6,@H'FFA1        ; same as above
;   mov.b @(8,SP),r0I       ; Get CCR
;   mov.b r0I,@_ccr_save; Save CCR to RAM
;   jsr   @_rtm_h8          ; Real Time Monitor Core routine
;   pop.l er1
;   pop.l er0
;   rte
;
timV_not_rtm:      ; DEBUG PURPOSE ONLY!!

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```
;      bclr  #CMFA,@TCSRVR ; stop interrupt
      bclr  #6,@H'FFA1      ; same as above
      rte

; -- Interrupt request Enable --
_irq_enable:
      andc.b #b'01111111,ccr ; Clear interrupt Mask
      rts

; -- Interrupt request Disable --
_irq_disable:
      orc.b  #b'10000000,ccr ; Set interrupt Mask
      rts

      .END
```