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/******
```

Timer V control

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November 4,2001 Separate Timer V control routine
November 17,2001
July 19,2004
November 7,2004 Start with New PCB, disable P76 toggle output
September 18,2005 Change to H8/3694F
October 1,2005

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```
*****/
```

```
// #pragma interrupt timerV_irq(VECT=22)
```

```
/* ----< Include Files > ----- */
```

```
#include "iodefine.h"
```

```
/* ----< Function Prototype > ----- */
```

```
void reset_timerV(void);
```

```
/* ----< Control program > ----- */
```

```
/* TIMERV Initialize routine */
```

```
void reset_timerV()
```

```
{  
    TV.TCRV0.BIT.CMIEA = 0x00;    /* Disable interrupt */  
    TV.TCRV0.BIT.CCLR = 0x01;    /* Clear TCNTV/Compare match A */  
    // TV.TCRV0.BIT.CKS = 0x03;    /* 16MHz/(64*125) = 2KHz(500uS) */  
    TV.TCRV0.BIT.CKS = 0x03;    /* 20MHz/(64*156) = 2.003KHz(499.2uS 0.16%) */  
    TV.TCRV1.BIT.ICKS = 0x00;    /* Clock = 1/64 */  
    // TV.TCORR = 125;            /* compare 500uS data */  
    TV.TCORR = 156;            /* compare 500uS data */  
    TV.TCRV0.BIT.CMIEA = 0x01;    /* Enable interrupt */  
}
```

```
/* TIMERV Interrupt Handler */
```

```
/* Does NOT use (use same named subroutine in rtm_H8tiny.asm)
```

```
void timerV_irq()
```

```
{  
    if( TV.TCSR.V.BIT.CMFA == 1 ){  
        TV.TCSR.V.BIT.CMFA = 0;  
    }  
}
```

```
*/
```