

```

*****
;
;   H8 Real Time Monitor System start-up and assembler routine
;
;
;       May      25,1998      Kenji Arai Start modification for H8/3048F
;       October  3,1999      Debug C section
;       November 4,2001      start porting for H8/3664F
;       July     20,2002      Modified for Hitach C compiler
;
;
;   Copyright (C) 1998,'99,'00,2001,'02 Kenji Arai/JH1PJL
;   All rights reserved. Permission is granted to use, modify,or redistribute
;   this software so long as it is not sold or exploited for profit.;
;
;   THIS SOFTWARE IS PROVIDED AS IS AND WITHOUT WARRANTY OF ANY KIND,
;   EITHER EXPRESSED OR IMPLIED.
*****
;
;       July     17,2004      Modified for HEW3 environment
;
;
;   .cpu 300HN
;
;   .IMPORT_rtm_h8
;   .IMPORT _ccr_save
;
;   .EXPORT_irq_enable
;   .EXPORT_irq_disable
;   .EXPORT _timerV_irq
;
; Support for running on the ROM (Transfer initial data)
; Interrupt enable/disable
; Tick timer using timer V
; Interrupt handler for serial communication
;
;   ;=====
;   ;   Vector area
;   ;=====
;   .SECTION      $VECT22,DATA,LOCATE=H'2C
;   .DATA.W      _timerV_irq ;Timer V Interrupt handler
;
;   ;-----
;   ;500uSec interval Interrupt
;   ;-----
;   .SECTION      P,CODE,ALIGN=2
;
; -- Timer Interrupt Handler --
_timerV_irq:
;   bra    timV_not_rtm      ; DEBUG PURPOSE ONLY!!
;   push.l er0
;   push.l er1
;   bclr  #CMFA,@TCSRVRV ; stop interrupt
;   bclr  #6,@H'FFA1      ; same as above
;   mov.b @(8,SP),r0I      ; Get CCR
;   mov.b r0I,@_ccr_save ; Save CCR to RAM
;   jsr   @_rtm_h8        ; Real Time Monitor Core routine
;   pop.l er1
;   pop.l er0
;   rte

timV_not_rtm:
;   bclr  #CMFA,@TCSRVRV ; stop interrupt

```

```
        bclr  #6,@H'FFA1      ; same as above
        rte

; -- Interrupt request Enable --
_irq_enable:
        andc.b #b'01111111,ccr ; Clear interrupt Mask
        rts

; -- Interrupt request Disable --
_irq_disable:
        orc.b #b'10000000,ccr ; Set interrupt Mask
        rts

        .END
```