

CN	Pin#	Port	No.	IC Pin#	Special	In/Out	DDS	Pin	OTHER	Pin	Project assignment	Special notice
1	20	P1	0	21	TMOW	O					OPEN	
x	x	P1	1	x	x	X	x	x	x	x	No define	x
x	x	P1	2	x	x	X	x	x	x	x	No define	x
x	x	P1	3	x	x	X	x	x	x	x	No define	x
2	20	P1	4	39	IRQ0	O	RESET				DDS Control	
2	21	P1	5	40	IRQ1	O	W_CLK				DDS Control	
2	22	P1	6	41	IRQ2	O	FQ_UD				DDS Control	
2	23	P1	7	42	IRQ3/TRGV	O	D7				DDS Control	
2	17	P2	0	36	SCK3	O					1mSec Out for Debug	
2	18	P2	1	37	RXD	I			EIA232		Serial Communication	Reserved
2	19	P2	2	38	TXD	O			EIA232		Serial Communication	Reserved
1	14	P5	0	15	WKP0	O					DDS Out(Max)	
1	15	P5	1	16	WKP1	O					DDS Out(+10dBm)	
1	16	P5	2	17	WKP2	O					DDS Out(0dBm)	
1	17	P5	3	18	WKP3	O					OPEN	
1	18	P5	4	19	WKP4	O					OPEN	
1	19	P5	5	20	WKP5/ADTRG	O					OPEN	
2	3	P5	6	22	SDA	I/O			SDA		EEPROM	
2	4	P5	7	23	SCL	O			SCL		EEPROM	
2	5	P7	4	24	TMRIV	O					OPEN	
2	6	P7	5	25	TMCIV	O					OPEN	
2	7	P7	6	26	TMOV	O					OPEN	
2	9	P8	0	28	FTCI	O			LED_Y		Communication Indicator	
2	10	P8	1	29	FTIOA	O					OPEN	
2	11	P8	2	30	FTIOB	O					OPEN	
2	12	P8	3	31	FTIOC	O					OPEN	
2	13	P8	4	32	FTIOD	O			PWM		OPEN	
2	14	P8	5	33	-	I					Mode select	Reserved
2	15	P8	6	34	-	O			LED_G		RUN Indicator	
2	16	P8	7	35	-	O			LED_R		STOP Indicator	
1	6	PB	0	4	AN0	I			CH0		RSSI	
1	5	PB	1	3	AN1	I			CH1		RSSI	
1	4	PB	2	2	AN2	I			Vref		Vref	
1	3	PB	3	1	AN3	I					GND	
x	x	PB	4	x	x	X	x	x	x	x	No define	x
x	x	PB	5	x	x	X	x	x	x	x	No define	x
x	x	PB	6	x	x	X	x	x	x	x	No define	x
x	x	PB	7	x	x	X	x	x	x	x	No define	x